DECLARATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or the below named s are the original, first and joint inventors (if plural names are listed below) of the subject matter which is

claimed and fe	or which a patent	is sought on the		ow) of the subject matter which is MICROCOMPUTER BRIDGE specification of which:
	is attached hereto. was filed on	as Application	Serial No	
I hereb	-	reviewed and under	stand the contents of	the above-identified specification,
	ntability of the subject			all information known to me to be riality" is defined in Title 37, Code
patent, United S any foreign appl	tates provisional appl	ication(s), or invented ited States provision	or's certificate listed bel	9 of any foreign application(s) for ow and have also identified below tor's certificate having a filing date
	PRIC	ORITY APPLICAT	ION(S)	Priority Claimed
(Number)	(Coun	try)	(Date Filed)	Yes/No
(Number)	(Coun	try)	(Date Filed)	Yes/No
listed below and United States ap acknowledge the claimed in this	I, insofar as the subject populication in the man eduty to disclose all application, as "mate e between the filing of	ct matter of each of ner provided by the information known eriality" is defined i	the claims of this appli first paragraph of Title to me to be material to n Title 37, Code of Fe	of any United States application(s) cation is not disclosed in the prior a 35, United States Code, § 112, I patentability of the subject matter deral Regulations, § 1.56, which or PCT international filing date of
(Appli	cation Serial No.)	(Filing Date)	(Status)	
(Appli	cation Serial No.)	(Filing Date)	(Status)	

I hereby direct that all correspondence and telephone calls be addressed to Louis H. Iselin, Williams, Morgan & Amerson, P.C., 7676 Hillmont, Suite 250, Houston, Texas 77040, (713) 934-4089.

I HEREBY DECLARE THAT ALL STATEMENTS MADE OF MY OWN KNOWLEDGE ARE TRUE AND THAT ALL STATEMENTS MADE ON INFORMATION AND BELIEF ARE BELIEVED TO BE TRUE; AND FURTHER THAT THESE STATEMENTS WERE MADE WITH THE KNOWLEDGE THAT WILLFUL FALSE STATEMENTS AND THE LIKE SO MADE ARE PUNISHABLE BY FINE OR IMPRISONMENT, OR BOTH, UNDER SECTION 1001 OF TITLE 18 OF THE UNITED STATES CODE AND THAT SUCH WILLFUL FALSE STATEMENTS MAY JEOPARDIZE THE VALIDITY OF THE APPLICATION OR ANY PATENT ISSUED THEREON.

Inventor's Full Name:	DALE E. GULICK	
Inventor's Signature:	Offellufa	
Country of Citizenship:	U.S.A.	Date: 10/3//01
Residence Address: (street,	11715 Astoria Drive	
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Declaration Inventor: Dale E. Gulick

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

DALE E. GULICK

Examiner: Unknown

Group Art Unit: Unknown

Attorney Docket: 2000.051500/TT4029

Serial No.: Unknown

Concurrently Herewith

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Filed:

For:

MICROCOMPUTER BRIDGE ARCHITECTURE WITH AN

EMBEDDED MICROCONTROLLER

POWER OF ATTORNEY

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

The undersigned, being the inventor named in the above-identified application, hereby revoke any previous Powers of Attorney and appoint:

Elizabeth A. Apperley, Reg. No. 36,428; Bradley Botsch, Reg. No. 34,552; Michael Caywood, Reg. No. 37,797; Daniel R. Collopy, Reg. No. 33,667; Paul S. Drake, Reg. No. 33,491; Rajendra Jaipershad, Reg. No. 44,168; Diana Roberts, Reg. No. 36,654; Richard J. Roddy, Reg. No. 27,688; and Harry A. Wolin, Reg. No. 32,638 of Advanced Micro Devices, Inc.; and

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as its attorney or agent so long as they remain with such firms, with full power of substitution and revocation, to prosecute the application, to make alterations and amendments therein, to transact all business in the Patent and Trademark Office in connection therewith, and to receive any Letters Patent, and for one year after issuance of such Letters Patent to file any request for a certificate of correction that may be deemed appropriate.

Please direct all communications as follows:

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Date: 10/31/01